OIPE IAP38
OCT 2 4 2005
PATENT & TRADEMARK OFFICE

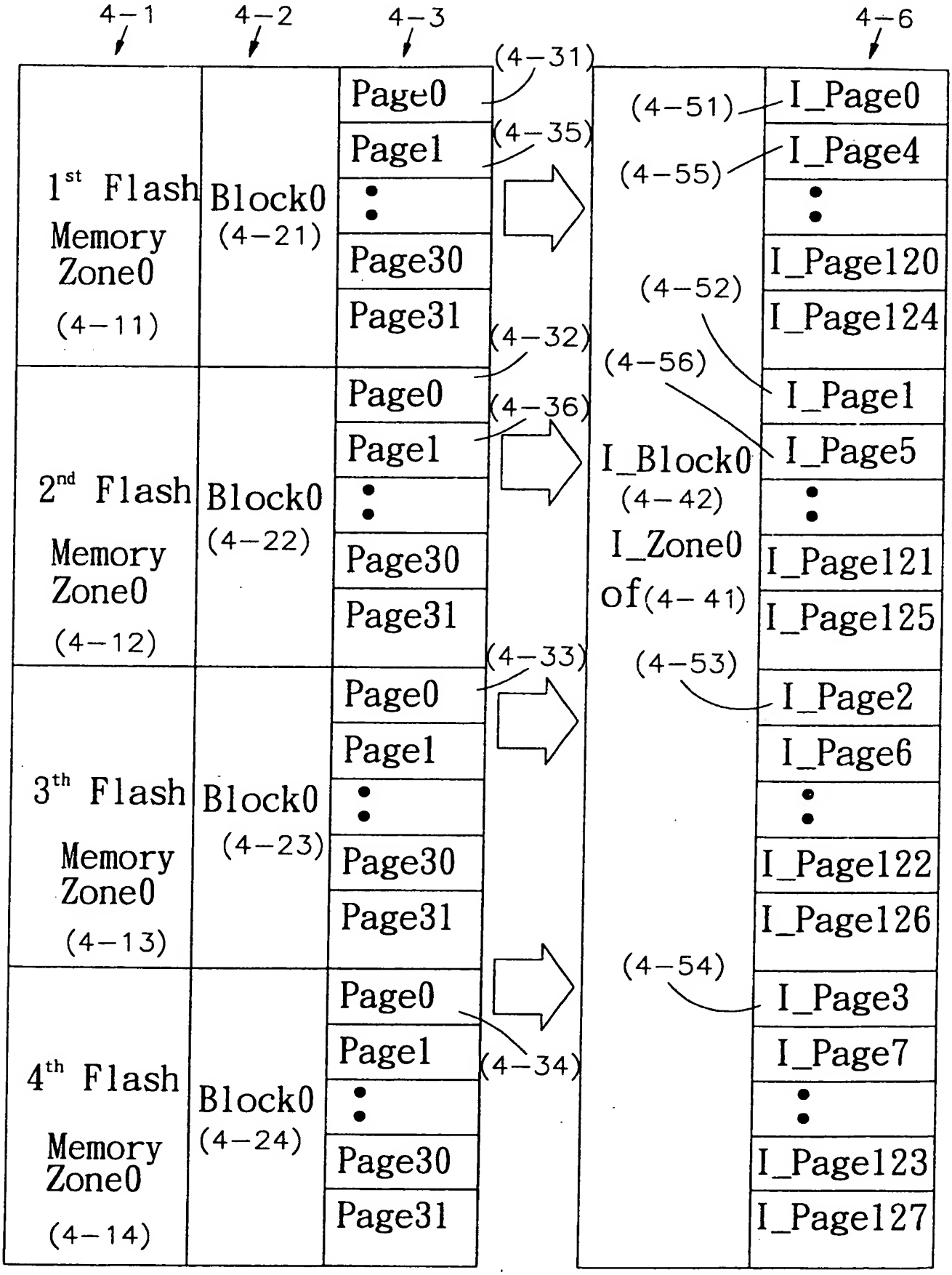

FIG. 4

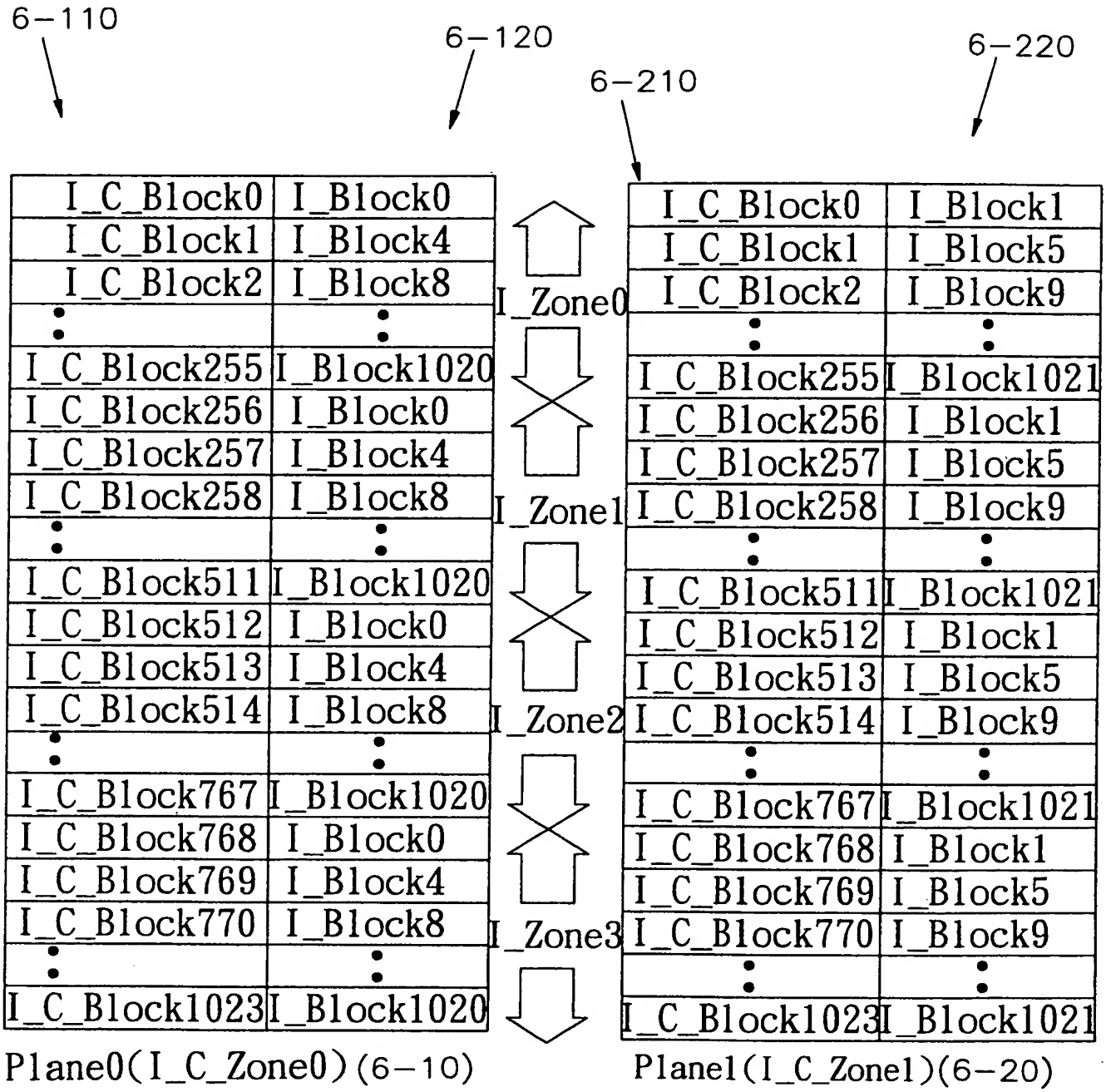
6-110
6-120
6-210
6-220
I_C_Block0 I_Block0
I_C_Block1 I_Block4
I_C_Block2 I_Block8
I_C_Block255 I_Block1020
I_C_Block256 I_Block0
I_C_Block257 I_Block4
I_C_Block258 I_Block8
I_C_Block511 I_Block1020
I_C_Block512 I_Block0
I_C_Block513 I_Block4
I_C_Block514 I_Block8
I_C_Block767 I_Block1020
I_C_Block768 I_Block0
I_C_Block769 I_Block4
I_C_Block770 I_Block8
I_C_Block1023 I_Block1020
I_Zone0
I_Zone1
I_Zone2
I_Zone3
I_C_Block0 I_Block1
I_C_Block1 I_Block5
I_C_Block2 I_Block9
I_C_Block255 I_Block1021
I_C_Block256 I_Block1
I_C_Block257 I_Block5
I_C_Block258 I_Block9
I_C_Block511 I_Block1021
I_C_Block512 I_Block1
I_C_Block513 I_Block5
I_C_Block514 I_Block9
I_C_Block767 I_Block1021
I_C_Block768 I_Block1
I_C_Block769 I_Block5
I_C_Block770 I_Block9
I_C_Block1023 I_Block1021
Plane0(I_C_Zone0)(6-10)
Plane1(I_C_Zone1)(6-20)

FIG. 6A

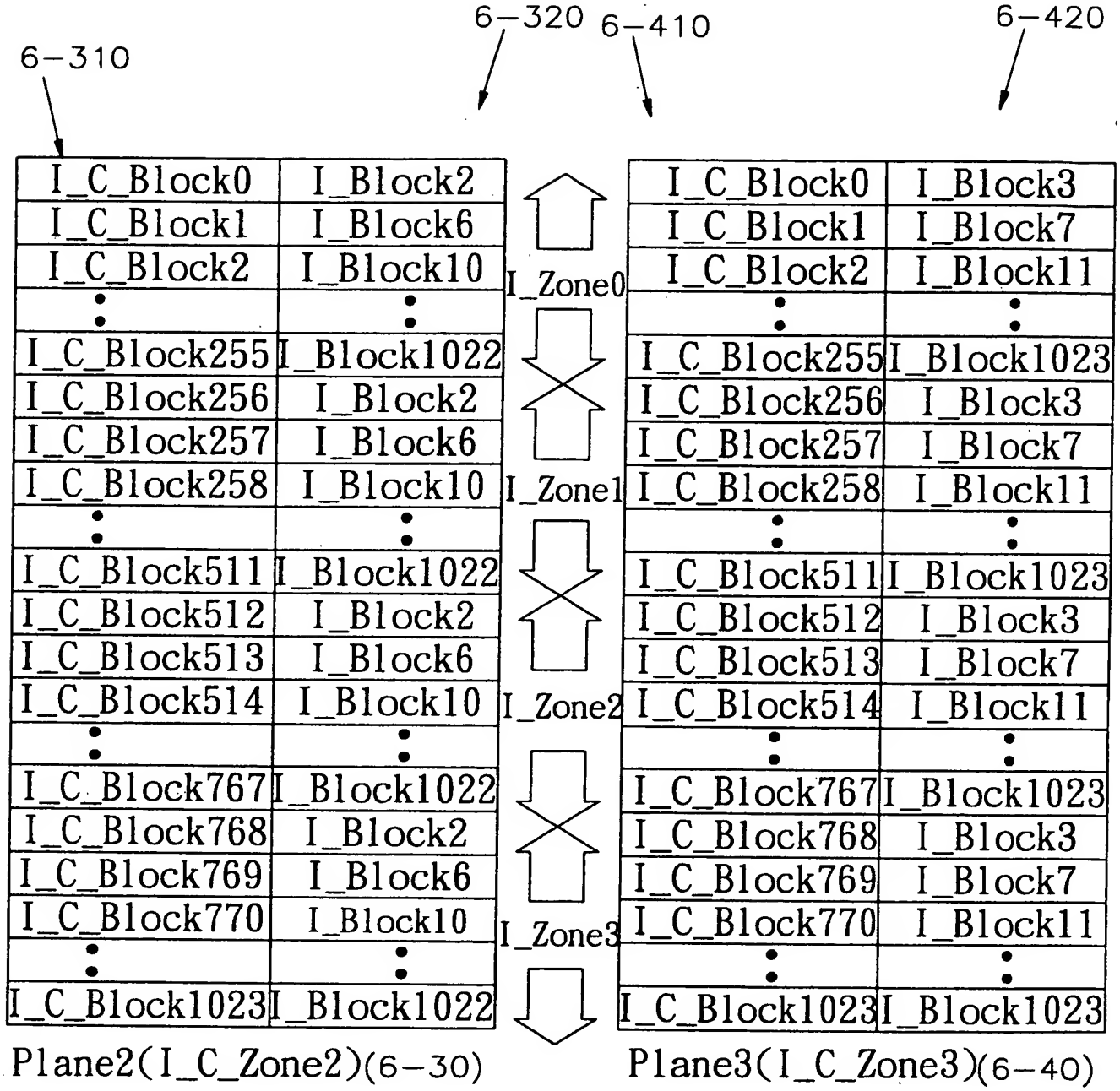
6-310
6-320
6-410
6-420
I_C_Block0
I_C_Block1
I_C_Block2
I_C_Block255
I_C_Block256
I_C_Block257
I_C_Block258
I_C_Block511
I_C_Block512
I_C_Block513
I_C_Block514
I_C_Block767
I_C_Block768
I_C_Block769
I_C_Block770
I_C_Block1023
I_Block2
I_Block6
I_Block10
I_Block1022
I_Block2
I_Block6
I_Block10
I_Block1022
I_Block2
I_Block6
I_Block10
I_Block1022
I_Block2
I_Block6
I_Block10
I_Block1022
I_Zone0
I_Zone1
I_Zone2
I_Zone3
I_C_Block0
I_C_Block1
I_C_Block2
I_C_Block255
I_C_Block256
I_C_Block257
I_C_Block258
I_C_Block511
I_C_Block512
I_C_Block513
I_C_Block514
I_C_Block767
I_C_Block768
I_C_Block769
I_C_Block770
I_C_Block1023
I_Block3
I_Block7
I_Block11
I_Block1023
I_Block3
I_Block7
I_Block11
I_Block1023
I_Block3
I_Block7
I_Block11
I_Block1023
I_Block3
I_Block7
I_Block11
I_Block1023
Plane2(I_C_Zone2)(6-30)
Plane3(I_C_Zone3)(6-40)

FIG. 6B

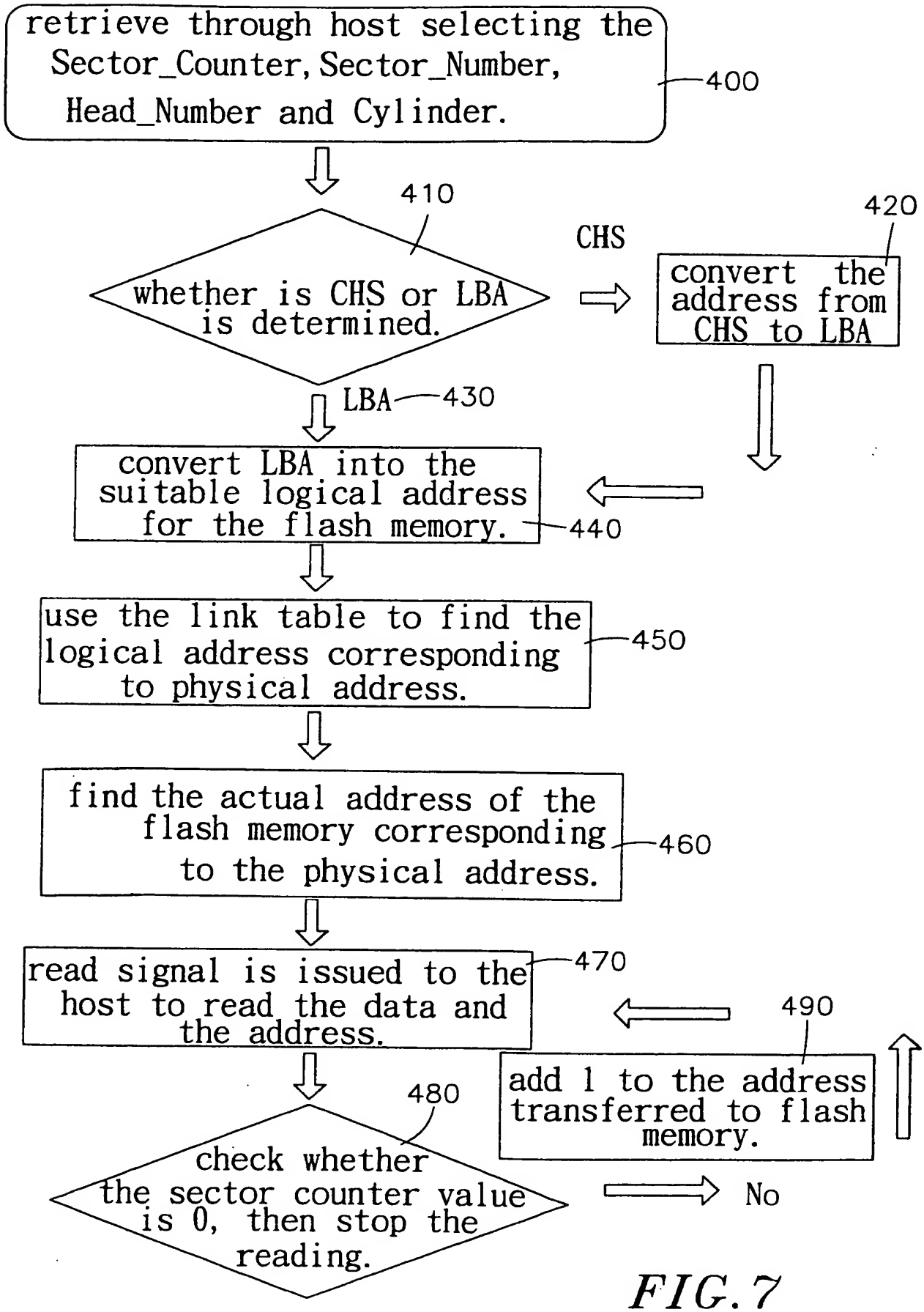
retrieve through host selecting the Sector_Counter, Sector_Number, Head_Number and Cylinder.
400
410
whether is CHS or LBA is determined.
CHS
420
convert the address from CHS to LBA
LBA
430
convert LBA into the suitable logical address for the flash memory.
440
use the link table to find the logical address corresponding to physical address.
450
find the actual address of the flash memory corresponding to the physical address.
460
read signal is issued to the host to read the data and the address.
470
490
add 1 to the address transferred to flash memory.
480
check whether the sector counter value is 0, then stop the reading.
No

FIG. 7

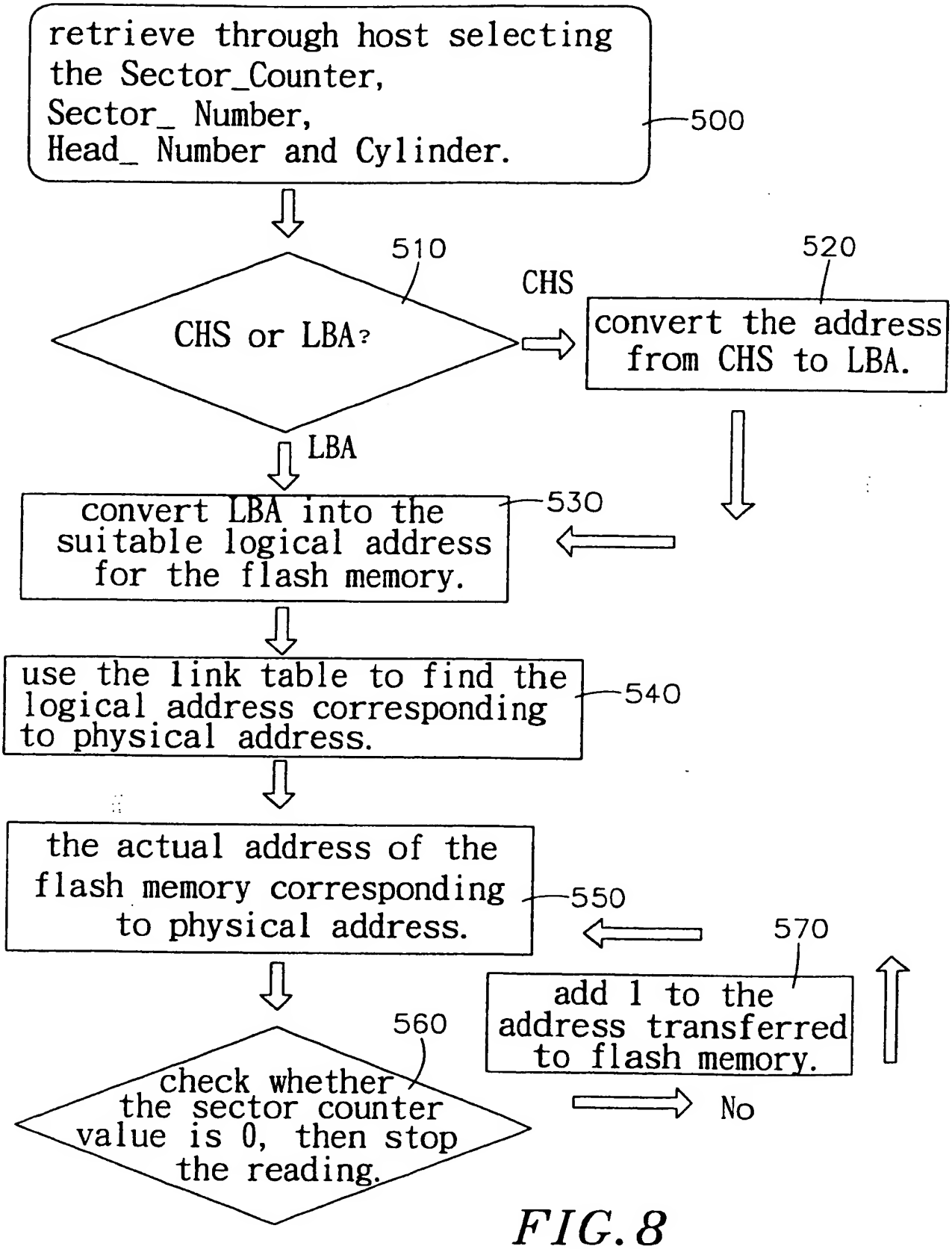
retrieve through host selecting the Sector_Counter, Sector_ Number, Head_ Number and Cylinder.
500
510
CHS or LBA?
CHS
520
convert the address from CHS to LBA.
LBA
convert LBA into the suitable logical address for the flash memory.
530
use the link table to find the logical address corresponding to physical address.
540
the actual address of the flash memory corresponding to physical address.
550
570
add 1 to the address transferred to flash memory.
560
check whether the sector counter value is 0, then stop the reading.
No

FIG. 8